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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/655,997
	Filing Date	September 5, 2003
	First Named Inventor	John P. Daley
	Art Unit	2812
	Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number	MI22-2380

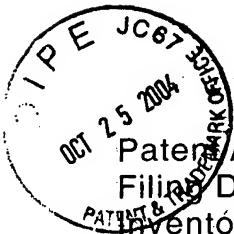
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Firm Name	Wells St. John P.S.	
Signature		
Printed name	Mark S. Matkin	
Date	10/25/04	Reg. No. 32,268

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EV372469998



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. .... 10/655,997  
Filing Date ..... September 5, 2003  
Inventor ..... Jon P. Daley  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2812  
Examiner ..... Unknown  
Attorney Docket No. .... MI22-2380  
Customer No. .... 021567  
Title: Methods of Forming Patterned Photoresist Layers over Semiconductor  
Substrates

**INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to 1276 OG 55, August 5, 2003, no copies of cited U.S. patents or U.S. patent application publications are included, as the date of filing of this patent application occurs after June 30, 2003. Copies of all other references are attached. No admission is made regarding whether all the listed references are prior art.

Citation of the referenced art is respectfully requested.

This Supplemental Information Disclosure Statement is being filed before the mailing date of a first Office Action. There, no fee is believed to be required.

However, in the event that a fee is required for filing this Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 10-25-04

By:   
Mark S. Matkin  
Reg. No. 32,268

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2380		SERIAL NO. 10/655,997	
<div style="position: relative; height: 100px;"> <div style="position: absolute; top: 0; left: 0; width: 100%; height: 100%; border: 1px solid black; border-radius: 50%; text-align: center; line-height: 100px; font-size: 24px; font-weight: bold;">O I P E J C E</div> <div style="position: absolute; top: 10%; left: 10%; font-size: 18px; font-weight: bold;">OCT 25 2004</div> <div style="position: absolute; bottom: 10%; left: 10%; font-size: 12px; font-weight: bold; transform: rotate(-45deg);">PATENT &amp; TRADEMARK</div> </div>				LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)			
				APPLICANT: Jon P. Daley			
				FILING DATE September 5, 2003		GROUP 2812	
U.S. PATENT DOCUMENTS							
*Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	3,349,474	12/1963	D. H. Rauscher			
	AB	5,904,517	05/1999	Gardner et al.	438	197	
	AC	5,998,264	12/1999	Wu	438	260	
	AD	6,180,465 B1	01/2001	Gardner et al.	438	291	
	AE	6,207,485 B1	03/2001	Gardner et al.	438	216	
	AF	6,548,854 B1	04/2003	Kizilyalli et al.	257	310	
	AG						
	AH	2003/0045060 A1	03/2003	Ahn et al.	438	287	
	AI	2003/0045078 A1	03/2003	Ahn et al.	438	585	
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes      No
	AJ	EP 0 851 473 A2	01/1998	EPO			X
	AK						
	AL						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AM		Chang et al., Silicon surface treatments in advanced MOS gate processing, Microelectronic Engineering, (2004), pages 130-135				
	AN		Lemberger et al., Electrical characterization and reliability aspects of zirconium silicate films obtained from novel MOCVD precursors, Microelectronic Engineering (2004), pages 315-320				
	AO		Lu et al., Effects of the TaN <sub>x</sub> interface layer on doped tantalum oxide high-k films, VACUUM (2004), pages 1-9				
EXAMINER		DATE CONSIDERED					
<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							

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*Examiner's Initials	AA	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS								
	AJ	Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AJ							
	AK							
	AL							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
AM		Robertson et al., Atomic structure, band offsets, growth and defects at high-K oxide:Si interfaces, Microelectronic Engineering (2004) pages 112-120	
AN		Singh et al., High and Low Dielectric Constant Materials, The Electrochemical Society <i>Interface</i> , Summer 1999, pages 26-30	
AO			
EXAMINER		DATE CONSIDERED	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.